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ে) n restne application of: Marcus A. BAKER, et al.

Serial No: 10/609,892

Filed: June 30, 2003

Confirmation No: 8340

Group Art Unit: 2113

Examiner: Urick, Matthew T.

For: METHOD AND SYSTEM FOR SAVING THE STATE OF INTEGRATED CIRCUITS UPON FAILURE

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Applicant recognizes that in accordance with M.P.E.P. § 1302.14, the Examiner's reasons for allowance need not set forth all of the details as to why the claims are allowed. In the above-referenced application, Applicant does not concede that the Examiner's stated reasons for allowance are the only reasons for which the claims are allowable. In particular, Applicant does not concede that the identified limitations with respect to independent claims 1, 14 and 27 are the only grounds for patentability of the allowed claims. Furthermore, the claims may be patentable for other reasons. In addition, Applicant notes that the dependent claims may also be allowable on their own merits, and are allowable on the basis of a sub-combination of the recited features of the dependent claims and their respective base claims.

Respectfully submitted,

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October 13, 2006

Date

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